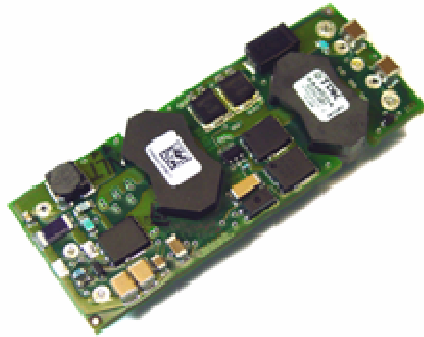


Bellesta® iEA Series DC/DC Power Modules
 48V Input, 4.5A Output
 Eighth Brick



The Bellesta Series offers an industry standard eighth brick power module with true useable power. Thanks to its low component count and single-board open-frame design, it provides both better electrical and thermal performance, and lower cost than many other suppliers' eighth brick product offerings. Bellesta® modules are perfect for both next generation planning and cost reduction design environments

Standard Features:

- Size – 58.4mm x 22.9 mm x 8.8 mm (2.3 in. x 0.9 in. x 0.347 in.)
- Thru-hole pins 3.68 mm (0.145")
- High efficiency – greater than 90%
- 1500Vdc isolation voltage
- Meets basic insulation spacing requirements
- Constant switching frequency
- Industry Standard Footprint
- Output Voltage Adjustment
- Remote on/off (positive logic)
- Remote sense
- Auto-recovering output over-voltage protection
- Auto-recovering output over-current protection
- Auto-recovering output short circuit protection
- Auto-recovering over-temperature protection

- UL 60950 (US and Canada), VDE 0805, CB scheme (IEC950), CE Mark (EN60950)
- ISO Certified manufacturing facilities
- Patented Design

Optional Features:

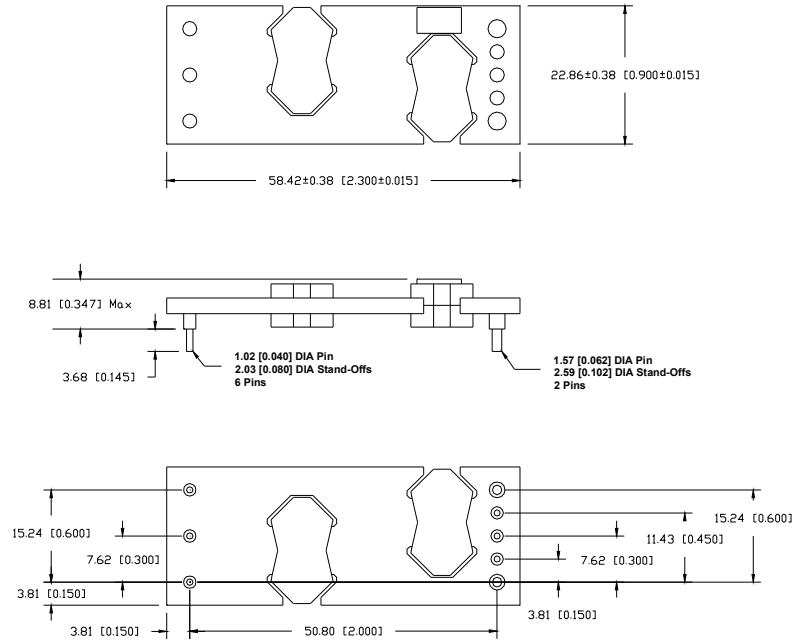
- Remote on/off (negative logic)
- Latching output over-voltage protection
- Short Thru-hole pins 2.79 mm (0.110")
- Long Thru-hole pins 5.08 mm (0.200")

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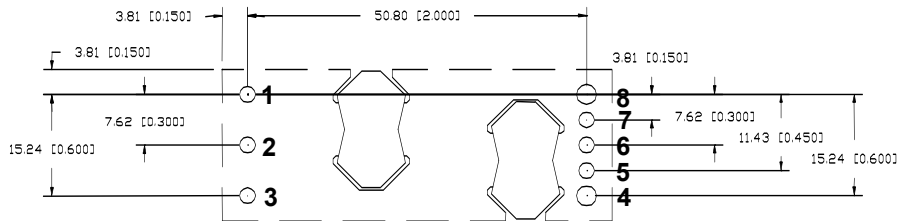
Advance Data Sheet: Belletra® iEA Series –Single Output Eighth Brick

Mechanical Specification:

Dimensions are in mm [in]. Unless otherwise specified tolerances are: $x.x \pm 0.5$ [0.02], $x.xx$ and $x.xxx \pm 0.25$ [0.010].



Recommended Hole Pattern: (top view)



Pin Assignment:

PIN	FUNCTION	PIN	FUNCTION
1	Vin(+)	4	Vo(-)
2	On/Off	5	Sense(-)
3	Vin(-)	6	Trim
		7	Sense(+)
		8	Vo(+)

Pin base material is brass with tin plating; the maximum module weight is 30.4g (1.07 oz).

Advance Data Sheet: Bellesta® iEA Series –Single Output Eighth Brick

Absolute Maximum Ratings:

Stress in excess of Absolute Maximum Ratings may cause permanent damage to the device.

Characteristic	Min	Max	Unit	Notes & Conditions
Continuous Input Voltage	-0.5	80	Vdc	
Transient Input Voltage	---	100	Vdc	100mS max.
Isolation Voltage	---	1500	Vdc	
Storage Temperature	-55	125	°C	
Operating Temperature Range (Tc)	-40	125	°C	Measured at the location specified in the thermal measurement figure. Maximum temperature varies with model number, output current, and module orientation – see curve in thermal performance section of the data sheet.

Input Characteristics:

Unless otherwise specified, specifications apply over all Rated Input Voltage, Resistive Load, and Temperature conditions.

Characteristic	Min	Typ	Max	Unit	Notes & Conditions
Operating Input Voltage	36	48	75	Vdc	
Maximum Input Current	---	---	4	A	Vin = 0 to Vin,max
Turn-on Voltage	---	33	---	Vdc	
Turn-off Voltage	26	30	---	Vdc	
Hysteresis	0.5	3	---	Vdc	
Startup Delay Time from application of input voltage (28V output)	---	45	---	mS	Vo = 0 to 0.1*Vo,nom; on/off =on, Io=Io,max, Tc=25°C
Startup Delay Time from application of input voltage (15V, 18V outputs)	---	10	---		
Startup Delay Time from on/off (28V output)	---	45	---	mS	Vo = 0 to 0.1*Vo,nom; Vin= Vin,nom, Io=Io,max, Tc=25°C
Startup Delay Time from on/off (15V, 18V outputs)	---	10	---		
Output Voltage Rise Time (28V output)	---	35	---	mS	Io=Io,max,Tc=25°C, Vo=0.1 to 0.9*Vo,nom
Output Voltage Rise Time (15V, 18V outputs)	---	8	---		
Inrush Transient	---	---	0.2	A²s	
Input Reflected Ripple	---	15*	---	mApp	See input/output ripple and noise measurements figure; BW = 20 MHz
Input Ripple Rejection	---	55*	---	dB	@120Hz

Caution: The power modules are not internally fused. An external input line normal blow fuse with a maximum value of 10A is required, see the Safety Considerations section of the data sheet.

Advance Data Sheet: Bellesta[®] iEA Series –Single Output Eighth Brick

Electrical Data:

iEA48003A280V-000 through -007: 28V, 2.67A Output

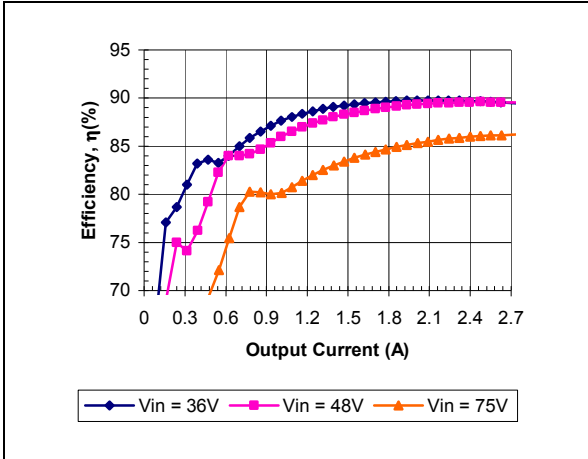
Characteristic	Min	Typ	Max	Unit	Notes & Conditions
Output Voltage Initial Setpoint	27.16	28	28.84	Vdc	Vin=Vin,nom; Io=Io,max; Tc = 25°C
Output Voltage Tolerance	26.88	28	29.12	Vdc	Over all rated input voltage, load, and temperature conditions to end of life
Efficiency	---	90	---	%	Vin=Vin,nom; Io=Io,max; Tc = 25°C
Line Regulation	---	20	70	mV	Vin=Vin,min to Vin,max
Load Regulation	---	15	70	mV	Io=Io,min to Io,max
Temperature Regulation	---	90	450	mV	Tc=Tc,min to Tc,max
Output Current	0.3*	---	2.67	A	At loads less than Io,min the module will continue to regulate the output voltage, but the output ripple may increase
Output Current Limiting Threshold	---	4	---	A	Vo = 0.9*Vo,nom, Tc<Tc,max
Short Circuit Current	---	3	---	A	Vo = 0.25V, Tc = 25°C
Output Ripple and Noise Voltage	---	80	250*	mVpp	Measured across one 3.3 uF ceramic capacitor and a 10uF tantalum capacitor – see input/output ripple measurement figure; BW = 20MHz
	---	20	---	mVrms	
Output Voltage Adjustment Range	70	---	110	%Vo,nom	
Output Voltage Sense Range	---	---	10	%Vo,nom	
Dynamic Response: Recovery Time	---	100	---	uS	di/dt = 0.1A/uS, Vin=Vin,nom; load step from 50% to 75% of Io,max
Transient Voltage	---	370	---	mV	
Output Voltage Overshoot during startup	---	---	10*	%	Vin=Vin,nom; Io=Io,max, Tc=25°C
Switching Frequency	---	450	---	KHz	Fixed
Output Over Voltage Protection	32*	---	38*	V	
External Load Capacitance	0	---	470&*	uF	
Isolation Capacitance	---	1000	---	pF	
Isolation Resistance	10	---	---	MΩ	
Vref		1.225		V	Required for trim calculation

& Contact TDK Innoveta for applications that require additional capacitance or very low esr

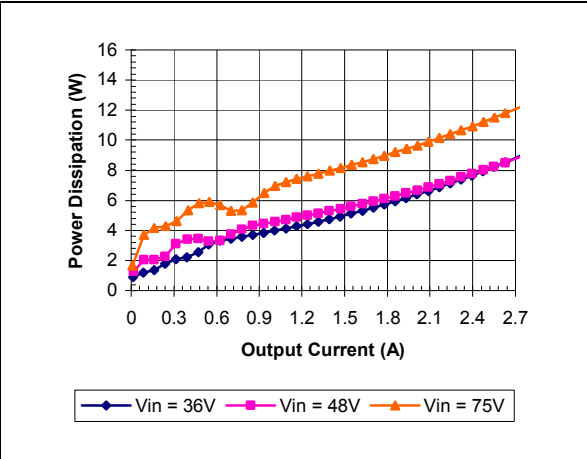
* engineering estimate

Electrical Characteristics (continued):

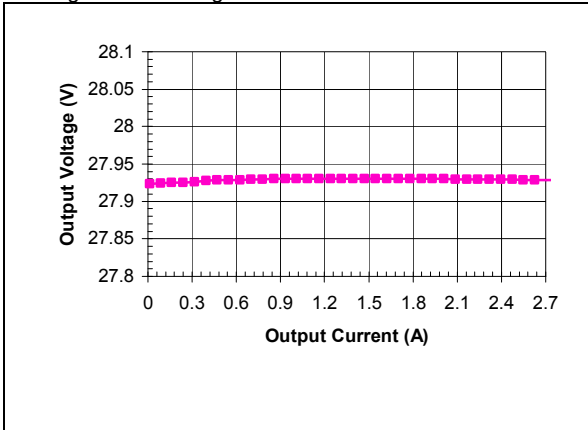
iEA48003A280V-000 through -007: 28V, 2.67A Output



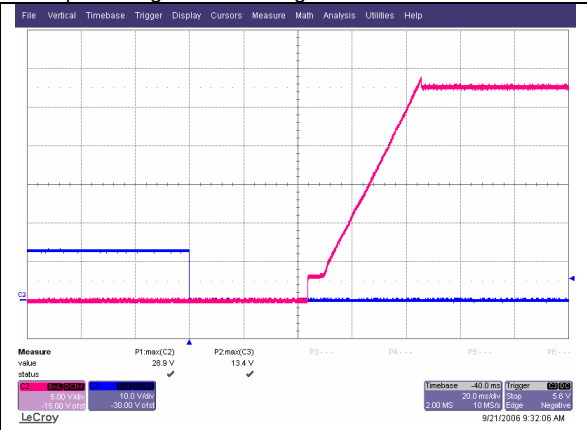
iEA48003A280V-000 Typical Efficiency vs. Input Voltage at Ta=25 degrees.



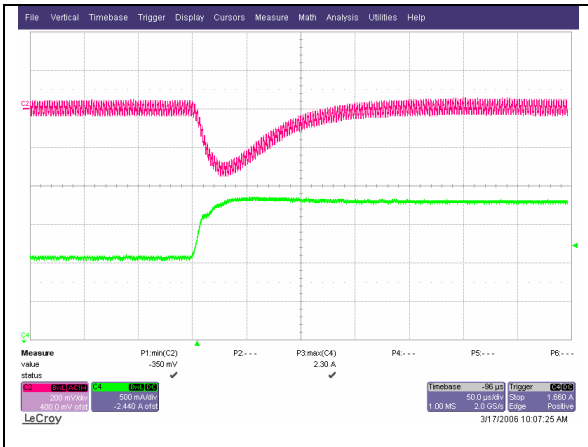
iEA48003A280V-000 Typical Power Dissipation vs. Input Voltage at Ta=25 degrees



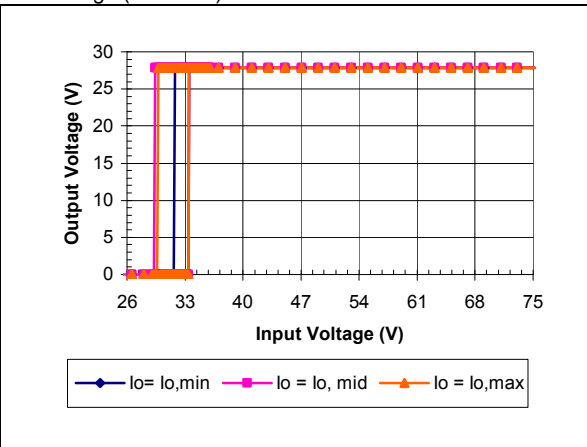
iEA48003A280V-000 Typical Output Voltage vs. Load Current at Ta = 25 degrees and Vin = 48V.



iEA48003A280V-001 Typical startup characteristic from on/off at full load. Blue - on/off signal, Red – output voltage (20ms/div)



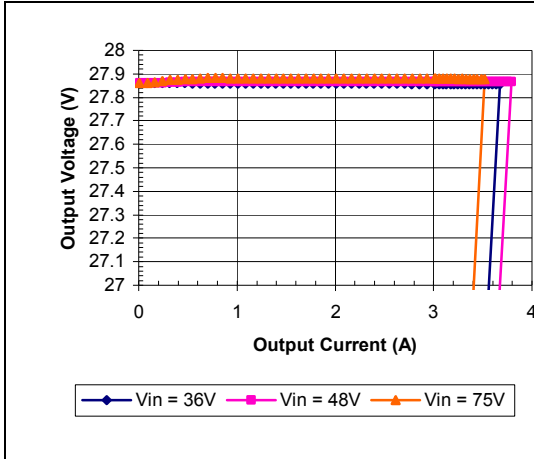
iEA48003A280V-000 Typical Output voltage response to load step from 50% to 75% of full load with output current slew rate of 0.1A/uS.



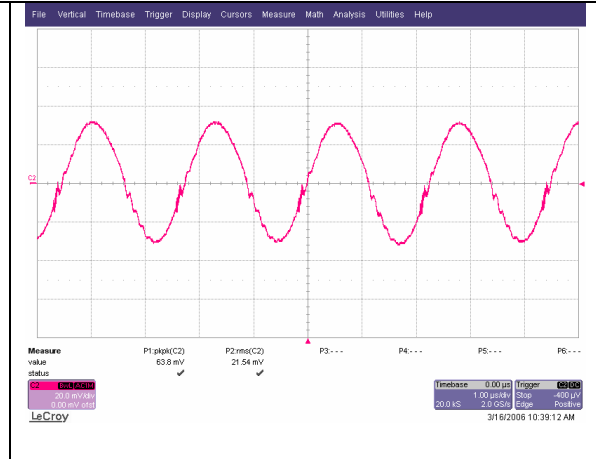
iEA48003A280V-000 Typical Output Voltage vs. Input Voltage Characteristics

Advance Data Sheet: Bellesta® iEA Series –Single Output Eighth Brick

Electrical Characteristics (continued): iEA48003A280V-000 through -007: 28V, 2.67A Output



iEA48003A280V-000 Typical Output Current Limit Characteristics vs. Input Voltage at Ta=25 degrees.



iEA48003A280V-000 Typical Output Ripple at nominal input voltage and full load at Ta=25 degrees (20mV/div)

% Change of Vout	Trim Down Resistor	% Change of Vout	Trim Up Resistor
-5%	51.9K	+5%	1372K
-10%	21.9K	+10%	716K

e.g. trim up 5%

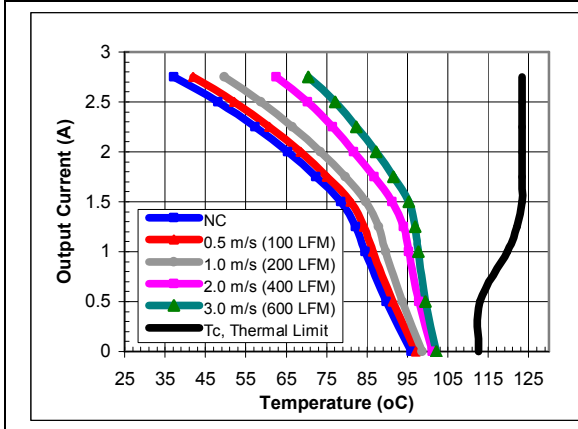
$$R_{up} := \left[\frac{3 \times 28 \cdot (100 + 5)}{1.225 \times 5} - \frac{300}{5} - 8.1 \right] \cdot K$$

iEA48003A280V-000 Calculated resistor values for output voltage adjustment

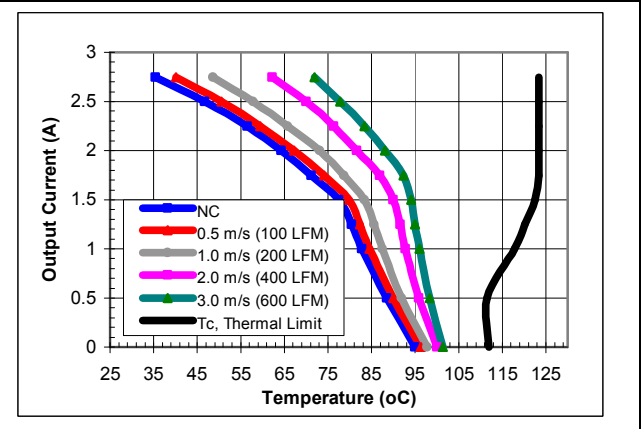
Advance Data Sheet: Belletra® iEA Series –Single Output Eighth Brick

Thermal Performance:

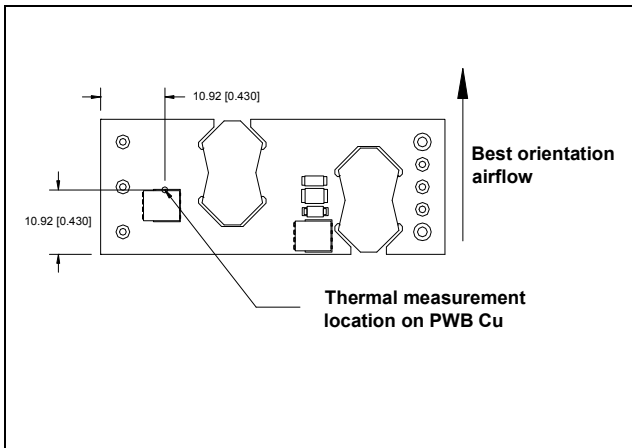
iEA48003A280V-000 through -007: 28V, 2.67A Output



iEA48003A280V-000 maximum output current vs. ambient temperature at nominal input voltage for airflow rates natural convection (60lfm) to 600lfm with airflow from pin 3 to pin 1.



iEA48003A280V-000 maximum output current vs. ambient temperature at nominal input voltage for airflow rates natural convection (60lfm) to 600lfm with airflow from pin 1 to pin 3.



iEA48003A280V-000 thermal measurement location – top view

The thermal curves provided above are based upon measurements made in TDK Innoveta's experimental test setup that is described in the Thermal Management section. Due to the large number of variables in system design, TDK Innoveta recommends that the user verify the module's thermal performance in the end application. The critical component should be thermo coupled and monitored, and should not exceed the temperature limit specified in the derating curve above. It is critical that the thermocouple be mounted in a manner that gives direct thermal contact or significant measurement errors may result. TDK Innoveta can provide modules with a thermocouple pre-mounted to the critical component for system verification tests.

Advance Data Sheet: Bellesta® iEA Series –Single Output Eighth Brick

Electrical Data:

iEA48004A180V-000 through -007: 18V, 3.75A Output

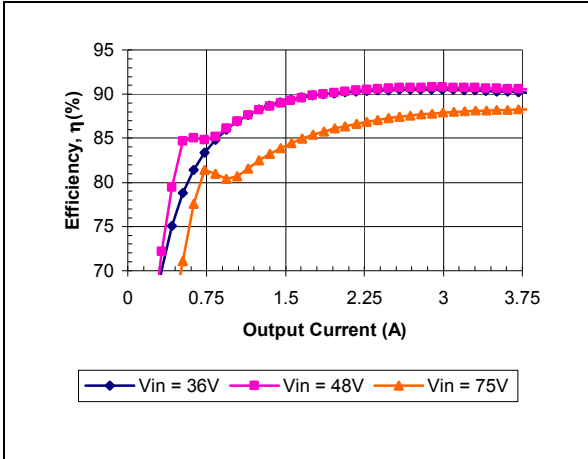
Characteristic	Min	Typ	Max	Unit	Notes & Conditions
Output Voltage Initial Setpoint	17.5	18	18.5	Vdc	Vin=Vin,nom; Io=Io,max; Tc = 25°C
Output Voltage Tolerance	17.28	18	18.72	Vdc	Over all rated input voltage, load, and temperature conditions to end of life
Efficiency	---	90.5	---	%	Vin=Vin,nom; Io=Io,max; Tc = 25°C
Line Regulation	---	10	45	mV	Vin=Vin,min to Vin,max
Load Regulation	---	10	45	mV	Io=Io,min to Io,max
Temperature Regulation	---			mV	Tc=Tc,min to Tc,max
Output Current	0.37	---	3.75	A	At loads less than Io,min the module will continue to regulate the output voltage, but the output ripple may increase
Output Current Limiting Threshold	---	4.4	---	A	Vo = 0.9*Vo,nom, Tc<Tc,max
Short Circuit Current	---	1	---	A	Vo = 0.25V, Tc = 25°C
Output Ripple and Noise Voltage	---	50	200*	mVpp	Measured across one 3.3 uF ceramic capacitor and a 10uF tantalum capacitor – see input/output ripple measurement figure; BW = 20MHz
	---	15	---	mVrms	
Output Voltage Adjustment Range	90	---	110	%Vo,nom	Module should be trimmed down or left at nominal voltage until input voltage is above 40Vin.
Output Voltage Sense Range	---	---	10	%Vo,nom	
Dynamic Response: Recovery Time	---	100	---	uS	di/dt = 0.1A/uS, Vin=Vin,nom; load step from 50% to 75% of Io,max
Transient Voltage	---	300	---	mV	
Output Voltage Overshoot during startup	---	---	10*	%	Vin=Vin,nom; Io=0.5*Io,max,Tc=25°C
Switching Frequency	---	375	---	KHz	Fixed
Output Over Voltage Protection	20*	---	26*	V	
External Load Capacitance	0	---	1000&	uF	
Isolation Capacitance	---	1000	---	pF	
Isolation Resistance	10	---	---	MΩ	
Vref		1.225		V	Required for trim calculation

& Contact TDK Innoveta for applications that require additional capacitance or very low esr

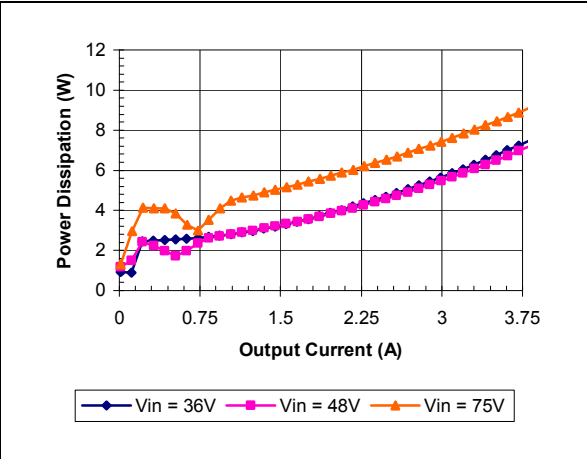
* engineering estimate

Electrical Characteristics (continued):

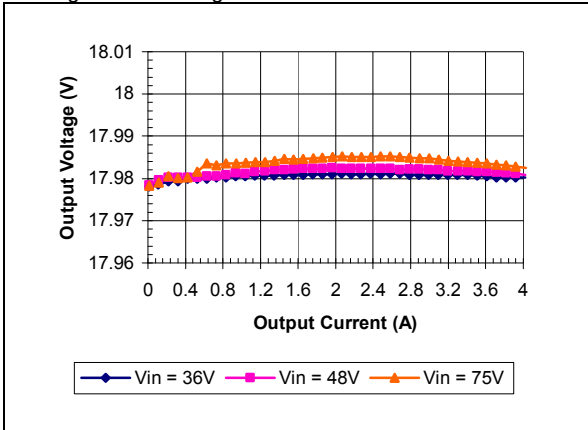
iEA48004A180V-000 through -007: 18V, 3.75A Output



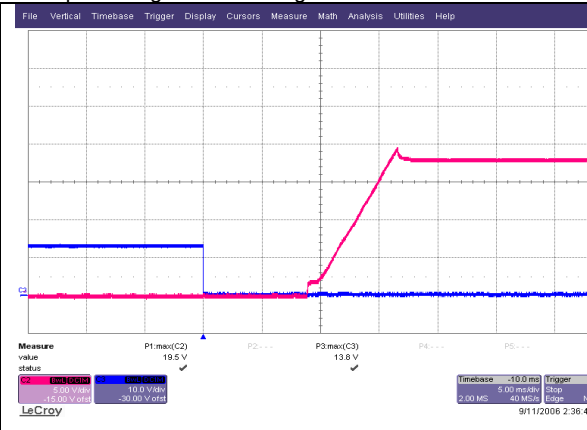
iEA48004A180V-000 Typical Efficiency vs. Input Voltage at Ta=25 degrees.



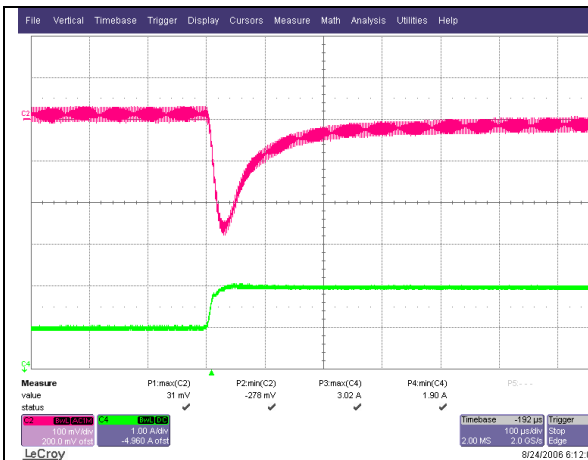
iEA48004A180V-000 Typical Power Dissipation vs. Input Voltage at Ta=25 degrees



iEA48004A180V-000 Typical Output Voltage vs. Load Current at Ta = 25 degrees



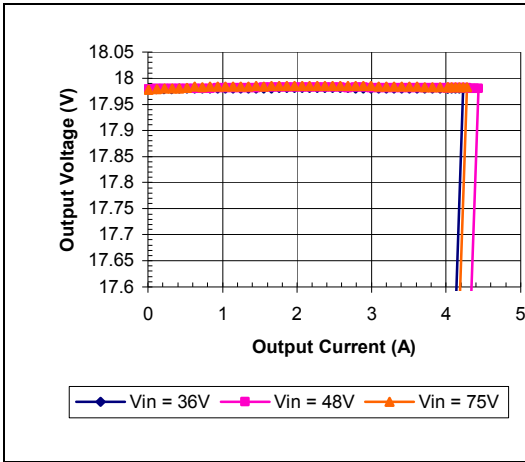
iEA48004A180V-001 Typical startup characteristic from on/off at full load. Ch 3 - on/off signal, Ch 2 – output voltage (5ms/div)



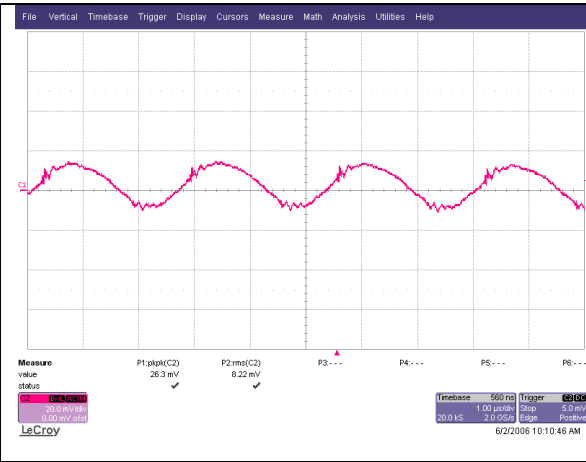
iEA48004A180V-000 Typical Output voltage response to load step from 50% to 75% of full load with output current slew rate of 0.1A/uS.

Advance Data Sheet: Belletra[®] iEA Series –Single Output Eighth Brick

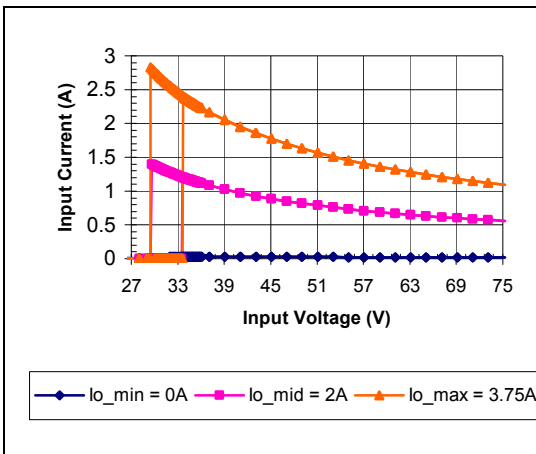
Electrical Characteristics (continued): iEA48004A180V-000 through -007: 18V, 3.75A Output



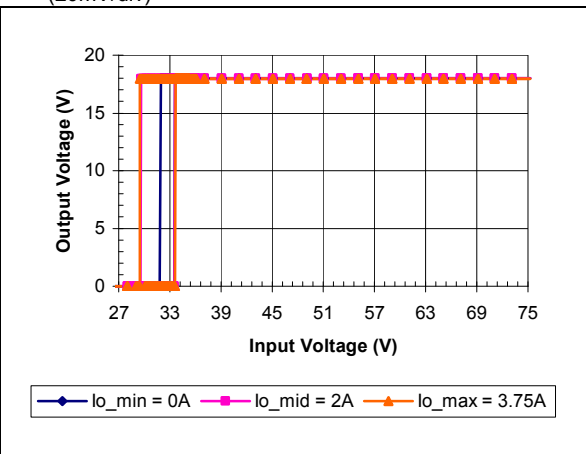
iEA48004A180V-000 Typical Output Current Limit Characteristics vs. Input Voltage at Ta=25 degrees.



iEA48004A180V-000 Typical Output Ripple at nominal input voltage and full load at Ta=25 degrees (20mV/div)



iEA48004A180V-000 Typical Input Current vs. Input Voltage Characteristics



iEA48004A180V-000 Typical Output Voltage vs. Input Voltage Characteristics

% Change of Vout	Trim Down Resistor	% Change of Vout	Trim Up Resistor
-5%	91.8K	+5%	1462K
-10%	40.8K	+10%	763K

e.g. trim up 5%

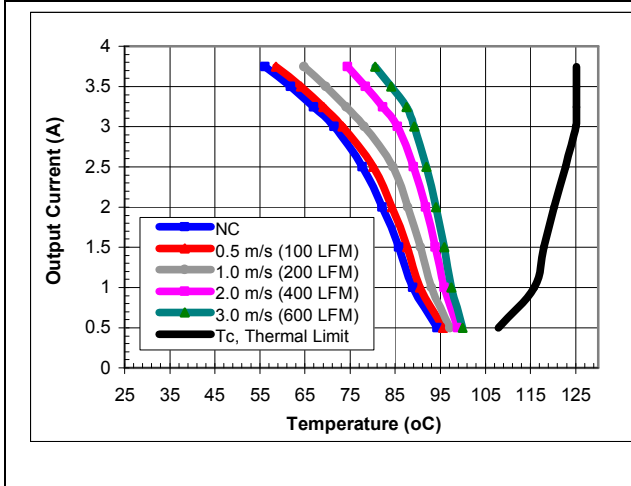
$$R_{up} := \left[\frac{5.1 \times 18 \cdot (100 + 5)}{1.225 \times 5} - \frac{510}{5} - 10.2 \right] \cdot K$$

iEA48004A180V-000 Calculated resistor values for output voltage adjustment

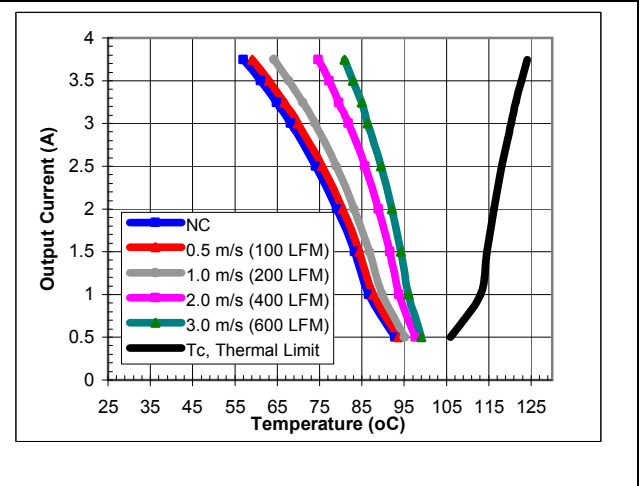
Advance Data Sheet: Belletra® iEA Series –Single Output Eighth Brick

Thermal Performance:

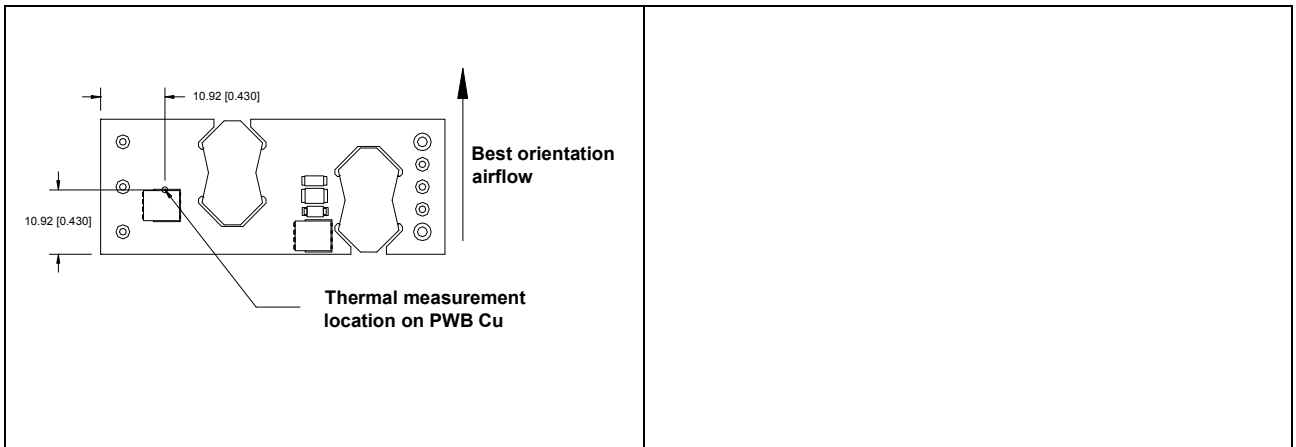
iEA48004A180V-000 through -007: 18V, 3.75A Output



iEA48004A180V-000 maximum output current vs. ambient temperature at nominal input voltage for airflow rates natural convection (60lfm) to 600lfm with airflow from pin 3 to pin 1.



iEA48004A180V-000 maximum output current vs. ambient temperature at nominal input voltage for airflow rates natural convection (60lfm) to 600lfm with airflow from pin 1 to pin 3.



iEA48004A180V-000 thermal measurement location – top view

The thermal curves provided above are based upon measurements made in TDK Innoveta's experimental test setup that is described in the Thermal Management section. Due to the large number of variables in system design, TDK Innoveta recommends that the user verify the module's thermal performance in the end application. The critical component should be thermo coupled and monitored, and should not exceed the temperature limit specified in the derating curve above. It is critical that the thermocouple be mounted in a manner that gives direct thermal contact or significant measurement errors may result. TDK Innoveta can provide modules with a thermocouple pre-mounted to the critical component for system verification tests.

Advance Data Sheet: Bellesta® iEA Series –Single Output Eighth Brick

Electrical Data:

iEA48005A150V-000 through -007: 15V, 4.5A Output

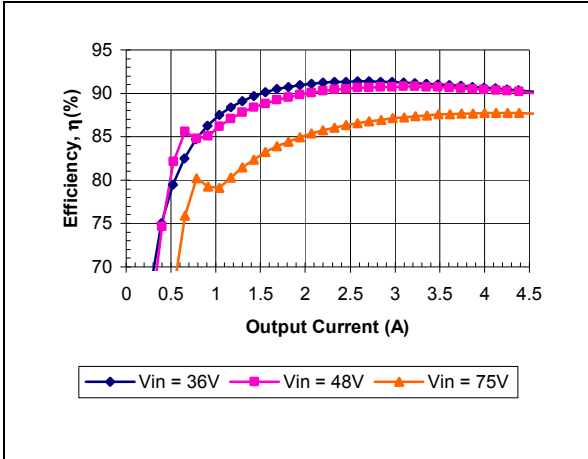
Characteristic	Min	Typ	Max	Unit	Notes & Conditions
Output Voltage Initial Setpoint	14.55	15	15.45	Vdc	Vin=Vin,nom; Io=Io,max; Tc = 25°C
Output Voltage Tolerance	14.47	15	15.52	Vdc	Over all rated input voltage, load, and temperature conditions to end of life
Efficiency	---	90	---	%	Vin=Vin,nom; Io=Io,max; Tc = 25°C
Line Regulation	---	10	35	mV	Vin=Vin,min to Vin,max
Load Regulation	---	10	35	mV	Io=Io,min to Io,max
Temperature Regulation	---	60	300	mV	Tc=Tc,min to Tc,max
Output Current	0.45*	---	4.5	A	At loads less than Io,min the module will continue to regulate the output voltage, but the output ripple may increase
Output Current Limiting Threshold	---	6	---	A	Vo = 0.9*Vo,nom, Tc<Tc,max
Short Circuit Current	---	9	---	A	Vo = 0.25V, Tc = 25°C
Output Ripple and Noise Voltage	---	40	150*	mVpp	Measured across one 3.3 uF ceramic capacitor and a 10uF tantalum capacitor – see input/output ripple measurement figure; BW = 20MHz
	---	10	---	mVrms	
Output Voltage Adjustment Range	90	---	110	%Vo,nom	
Output Voltage Sense Range	---	---	10	%Vo,nom	
Dynamic Response: Recovery Time	---	200	---	uS	di/dt = 0.1A/uS, Vin=Vin,nom; load step from 50% to 75% of Io,max
Transient Voltage	---	250	---	mV	
Output Voltage Overshoot during startup	---	---	10*	%	Vin=Vin,nom; Io=Io,max, Tc=25°C
Switching Frequency	---	300	---	KHz	Fixed
Output Over Voltage Protection	16.8	---	22	V	
External Load Capacitance	0	---	1500&	uF	
Isolation Capacitance	---	1000	---	pF	
Isolation Resistance	10	---	---	MΩ	
Vref		1.225		V	Required for trim calculation

& Contact TDK Innoveta for applications that require additional capacitance or very low esr

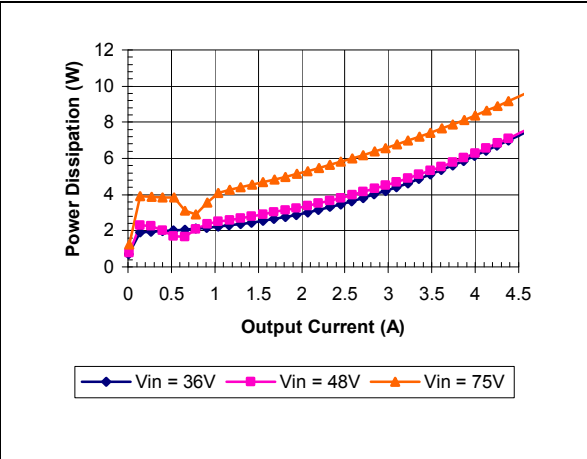
* engineering estimate

Electrical Characteristics (continued):

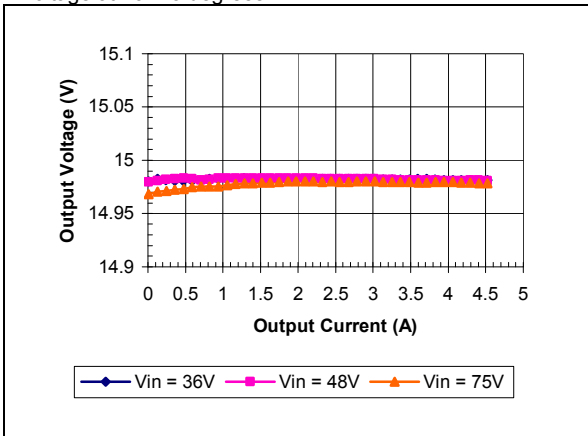
iEA48005A150V-000 through -007: 15V, 4.5A Output



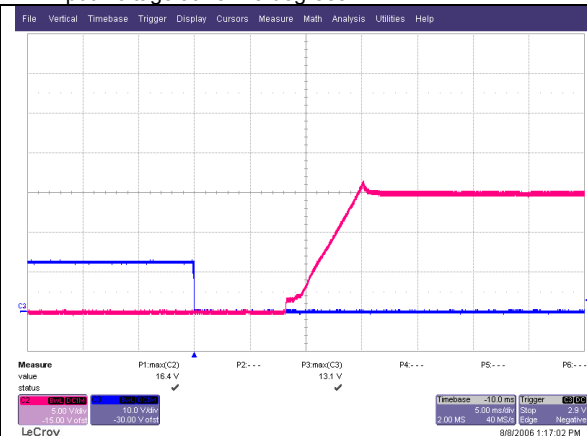
iEA48005A150V-000 Typical Efficiency vs. Input Voltage at Ta=25 degrees.



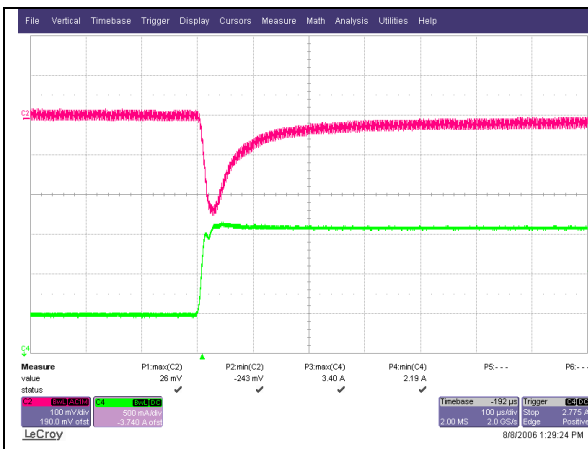
iEA48005A150V-000 Typical Power Dissipation vs. Input Voltage at Ta=25 degrees



iEA48005A150V-000 Typical Output Voltage vs. Load Current at Ta = 25 degrees



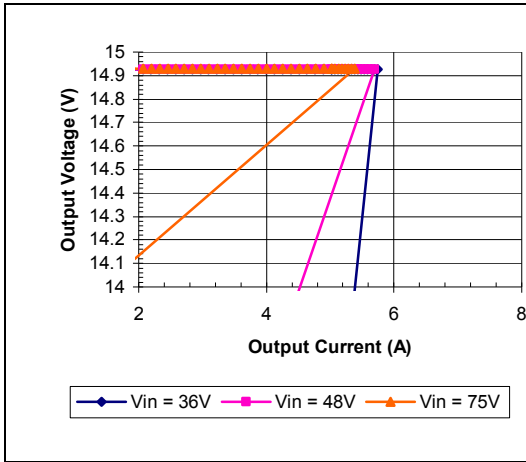
iEA48005A150V-001 Typical startup characteristic from on/off at full load. Ch 2 - on/off signal, Ch 1 – output voltage (5ms/div)



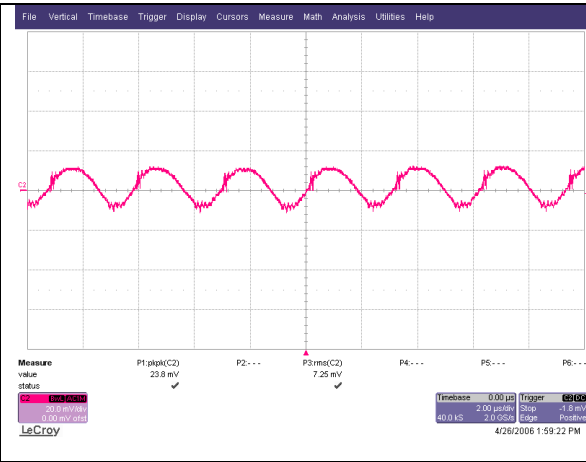
iEA48005A150V-000 Typical Output response to load step from 50% to 75% of full load with output current slew rate of 0.1A/uS. Ch2 – 100mV/div, 100uS/div

Advance Data Sheet: Bellesta[®] iEA Series –Single Output Eighth Brick

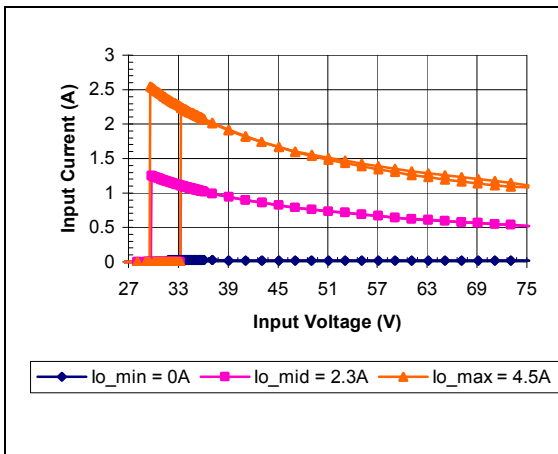
Electrical Characteristics (continued): iEA48005A150V-000 through -007: 15V, 4.5A Output



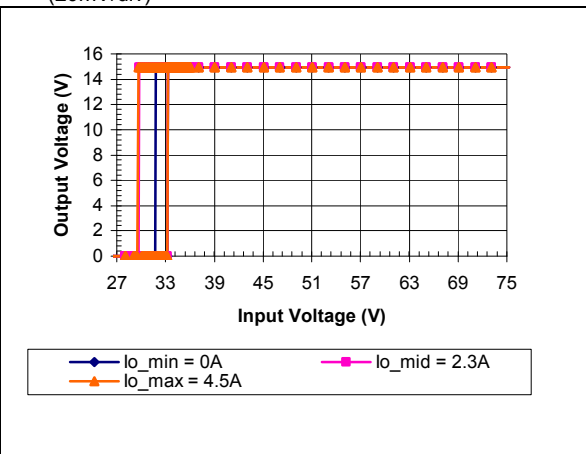
iEA48005A150V-000 Typical Output Current Limit Characteristics vs. Input Voltage at Ta=25 degrees.



iEA48005A150V-000 Typical Output Ripple at nominal input voltage and full load at Ta=25 degrees (20mV/div)



iEA48005A150V-000 Typical Input Current vs. Input Voltage Characteristics



iEA48005A150V-000 Typical Output Voltage vs. Input Voltage Characteristics

% Change of Vout	Trim Down Resistor (Kohm)	% Change of Vout	Trim Up Resistor (Kohm)
-5%	91.8K	+5%	1199K
-10%	40.8K	+10%	626K

e.g. trim up 5%

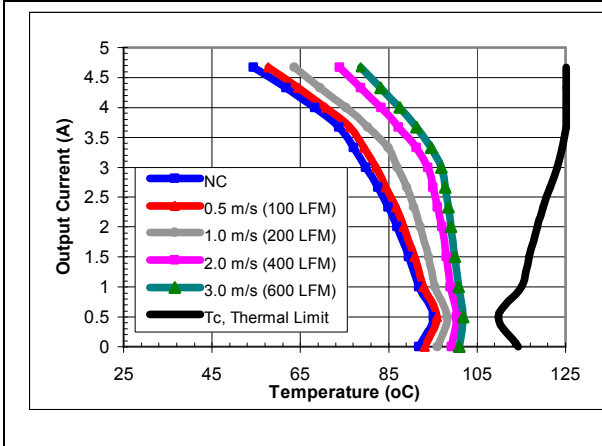
$$R_{up} := \left[\frac{5.1 \times 15 \cdot (100 + 5)}{1.225 \times 5} - \frac{510}{5} - 10.2 \right] \cdot K$$

iEA48005A150V-000 Calculated resistor values for output voltage adjustment

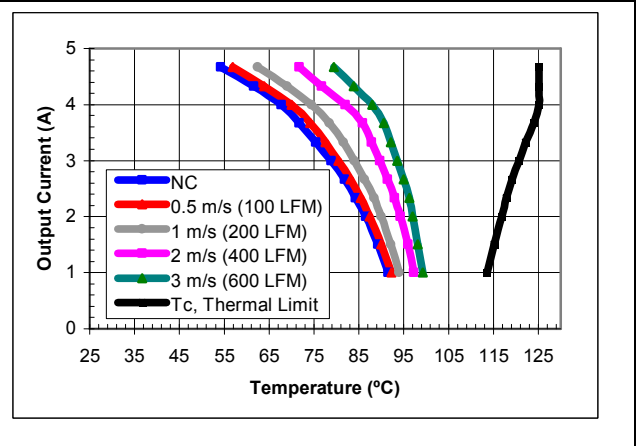
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Thermal Performance:

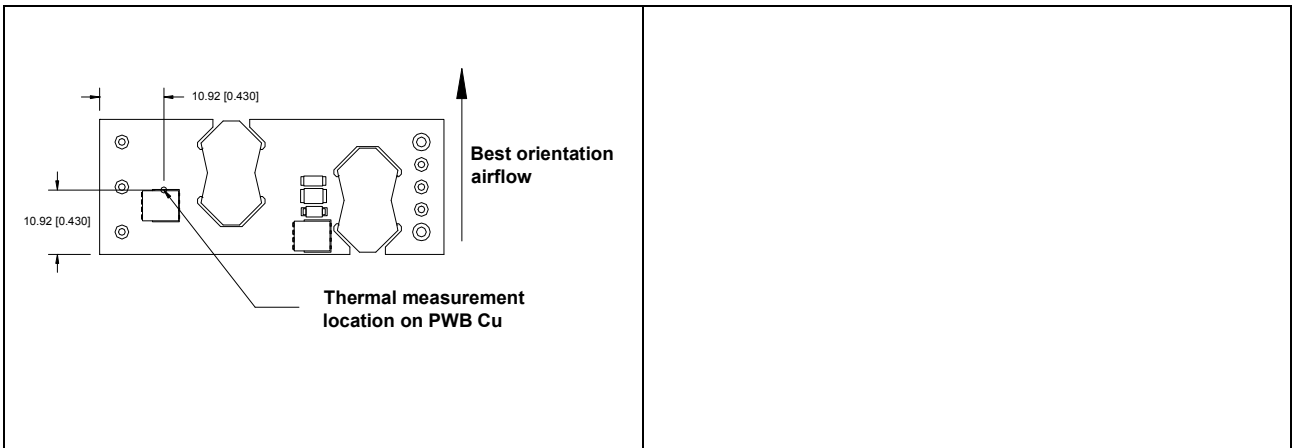
iEA48005A150V-000 through -007: 15V, 4.5A Output



iEA48005A150V-000 maximum output current vs. ambient temperature at nominal input voltage for airflow rates natural convection (60lfm) to 600lfm with airflow from pin 3 to pin 1.



iEA48005A150V-000 maximum output current vs. ambient temperature at nominal input voltage for airflow rates natural convection (60lfm) to 600lfm with airflow from pin 1 to pin 3.



iEA48005A150V-000 thermal measurement location – top view

The thermal curves provided above are based upon measurements made in TDK Innoveta's experimental test setup that is described in the Thermal Management section. Due to the large number of variables in system design, TDK Innoveta recommends that the user verify the module's thermal performance in the end application. The critical component should be thermo coupled and monitored, and should not exceed the temperature limit specified in the derating curve above. It is critical that the thermocouple be mounted in a manner that gives direct thermal contact or significant measurement errors may result. TDK Innoveta can provide modules with a thermocouple pre-mounted to the critical component for system verification tests.

Thermal Management:

An important part of the overall system design process is thermal management; thermal design must be considered at all levels to ensure good reliability and lifetime of the final system. Superior thermal design and the ability to operate in severe application environments are key elements of a robust, reliable power module.

A finite amount of heat must be dissipated from the power module to the surrounding environment. This heat is transferred by the three modes of heat transfer: convection, conduction and radiation. While all three modes of heat transfer are present in every application, convection is the dominant mode of heat transfer in most applications. However, to ensure adequate cooling and proper operation, all three modes should be considered in a final system configuration.

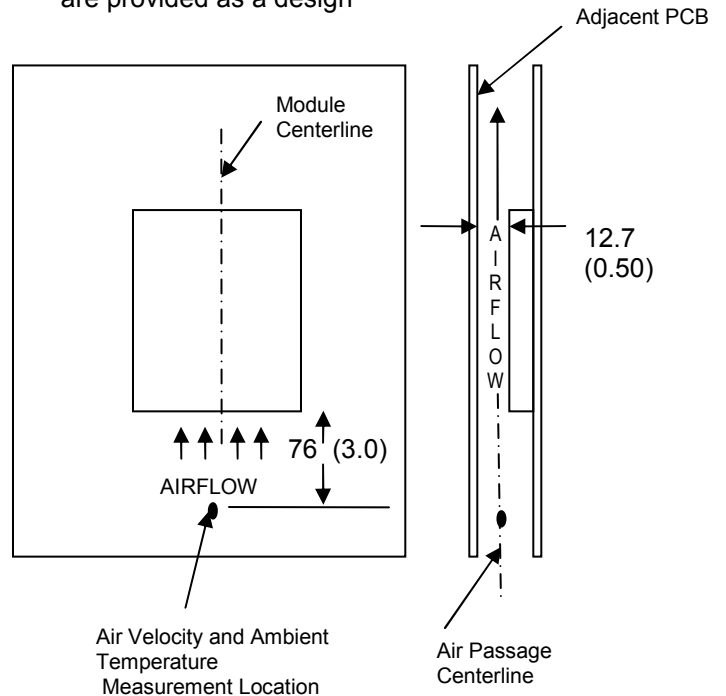
The open frame design of the power module provides an air path to individual components. This air path improves convection cooling to the surrounding environment, which reduces areas of heat concentration and resulting hot spots.

Test Setup: The thermal performance data of the power module is based upon measurements obtained from a wind tunnel test with the setup shown in the wind tunnel figure. This thermal test setup replicates the typical thermal environments encountered in most modern electronic systems with distributed power architectures. The electronic equipment in networking, telecom, wireless, and advanced computer systems operates in similar environments and utilizes vertically mounted PCBs or circuit cards in cabinet racks.

The power module, as shown in the figure, is mounted on a printed circuit board (PCB) and is vertically oriented within the wind tunnel. The cross section of the airflow passage is rectangular. The spacing between the top of the module and a parallel facing PCB is kept at a constant (0.5 in). The power module's orientation with respect

to the airflow direction can have a significant impact on the module's thermal performance.

Thermal Derating: For proper application of the power module in a given thermal environment, output current derating curves are provided as a design



Wind Tunnel Test Setup Figure Dimensions are in millimeters and (inches).

guideline on the Thermal Performance section for the power module of interest. The module temperature should be measured in the final system configuration to ensure proper thermal management of the power module. For thermal performance verification, the module temperature should be measured at the component indicated in the thermal measurement location figure on the thermal performance page for the power module of interest. In all conditions, the power module should be operated below the maximum operating temperature shown on the derating curve. For improved design margins and enhanced system reliability, the power module may be operated at temperatures below the maximum rated operating temperature.

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Heat transfer by convection can be enhanced by increasing the airflow rate that the power module experiences. The maximum output current of the power module is a function of ambient temperature (T_{AMB}) and airflow rate as shown in the thermal performance figures on the thermal performance page for the power module of interest. The curves in the figures are shown for natural convection through 2 m/s (400 ft/min). The data for the natural convection condition has been collected at 0.3 m/s (60 ft/min) of airflow, which is the typical airflow generated by other heat dissipating components in many of the systems that these types of modules are used in. In the final system configurations, the airflow rate for the natural convection condition can vary due to temperature gradients from other heat dissipating components.

Operating Information:

Over-Current Protection: The power modules have current limit protection to protect the module during output overload and short circuit conditions. During overload conditions, the power modules may protect themselves by entering a hiccup current limit mode. The modules will operate normally once the output current returns to the specified operating range. There is a typical delay of 30mS from the time an overload condition appears at the module output until the hiccup mode will occur.

Output Over-Voltage Protection: The power modules have a control circuit, independent of the primary control loop that reduces the risk of over voltage appearing at the output of the power module during a fault condition. If there is a fault in the primary regulation loop, the over voltage protection circuitry will cause the power module to enter a hiccup over-voltage mode once it detects that the output voltage has reached the level indicated on the Electrical Data section for the power module of interest. When the condition causing the over-voltage is corrected, the module will operate normally.

An optional latching over-voltage protection is available. On modules with this feature, the power module will shut down once it detects that the output voltage has reached the level indicated on the Electrical Data section for the power module of interest. The module remains off unless the input voltage is recycled.

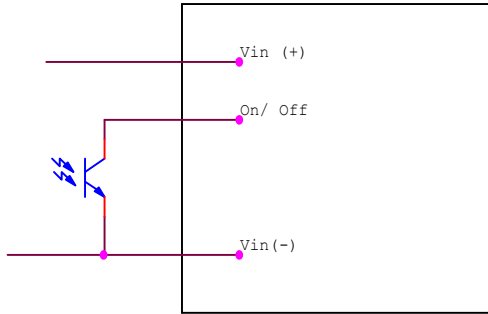
Thermal Protection: When the power modules exceed the maximum operating temperature, the modules may turn off to safeguard the power unit against thermal damage. The module will auto restart as the unit is cooled below the over temperature threshold. On modules with the latching over-voltage protection feature, the unit may latch off during a severe over temperature condition; the module remains off unless the input voltage is recycled.

Remote On/Off: - The power modules have an internal remote on/off circuit. The user must supply an open-collector or compatible switch between the Vin(-) pin and the on/off pin. The maximum voltage generated by the power module at the on/off terminal is 15V. The maximum allowable leakage current of the switch is 50uA. The switch must be capable of maintaining a low signal Von/off < 1.2V while sinking 1mA.

The standard on/off logic is positive logic. The power module will turn on if terminal 2 is left open and will be off if terminal 2 is connected to terminal 3. If the positive logic circuit is not being used, terminal 2 should be left open.

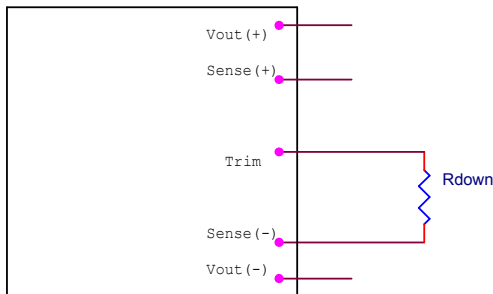
An optional negative logic is available. The power module will turn on if terminal 2 is connected to terminal 3, and it will be off if terminal 2 is left open. If the negative logic feature is not being used, terminal 2 should be shorted to terminal 3.

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On/Off Circuit for positive or negative logic

Output Voltage Adjustment: The output voltage of the power module may be adjusted by using an external resistor connected between the Vout trim terminal (pin 6) and either the Sense (+) or Sense (-) terminal. If the output voltage adjustment feature is not used, pin 6 should be left open. Care should be taken to avoid injecting noise into the power module's trim pin. A small 0.01uF capacitor between the power module's trim pin and Sense (-) pin may help avoid this.



Circuit to decrease output voltage

With a resistor between the trim and Sense (-) terminals, the output voltage is adjusted down. To adjust the output voltage down a percentage of Vout (%Vo) from Vo,nom, the trim resistor should be chosen according to the following equation:

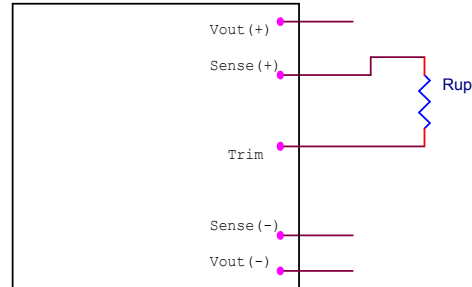
For all outputs except 28V:

$$R_{down} := \left(\frac{510}{\%Vo} - 10.2 \right) \cdot 1000$$

For 28V output:

$$R_{down} := \left(\frac{300}{\%Vo} - 8.1 \right) \cdot 1000$$

The current limit set point does not increase as the module is trimmed down, so the available output power is reduced.



Circuit to increase output voltage

With a resistor between the trim and sense (+) terminals, the output voltage is adjusted up. To adjust the output voltage up a percentage of Vout (%Vo) from Vo,nom the trim resistor should be chosen according to the following equation:

For all outputs except 28V:

$$R_{up} := \left[\frac{5.1V_{onom} \cdot (100 + \%Vo)}{V_{ref} \%Vo} - \frac{510}{\%Vo} - 10.2 \right] \cdot 1000$$

For 28V output:

$$R_{up} := \left[\frac{3V_{onom} \cdot (100 + \%Vo)}{1.225 \%Vo} - \frac{300}{\%Vo} - 8.1 \right] \cdot 1000$$

The value of Vref is found in the Electrical Data section for the power module of interest. The maximum power available from the power module is fixed. As the output voltage is trimmed up, the maximum output current must be decreased to maintain the maximum rated power of the module. As the output voltage is trimmed, the output over-voltage set point is not adjusted. Trimming the output voltage too high may cause the output over voltage protection circuit to be triggered.

Advance Data Sheet: Belletra® iEA Series –Single Output Eighth Brick

Remote Sense: The power modules feature remote sense to compensate for the effect of output distribution drops. The output voltage sense range defines the maximum voltage allowed between the output power terminals and output sense terminals, and it is found on the electrical data page for the power module of interest. If the remote sense feature is not being used, the Sense(+) terminal should be connected to the Vo(+) terminal and the Sense (-) terminal should be connected to the Vo(-) terminal.

The output voltage at the Vo(+) and Vo(-) terminals can be increased by either the remote sense or the output voltage adjustment feature. The maximum voltage increase allowed is the larger of the remote sense range or the output voltage adjustment range; it is not the sum of both.

As the output voltage increases due to the use of the remote sense, the maximum output current must be decreased for the power module to remain below its maximum power rating.

EMC Considerations: TDK Innoveta power modules are designed for use in a wide variety of systems and applications. For assistance with designing for EMC compliance, please contact TDK Innoveta technical support.

Input Impedance:

The source impedance of the power feeding the DC/DC converter module will interact with the DC/DC converter. To minimize the interaction, a 10-100uF input electrolytic capacitor should be present if the source inductance is greater than 1.5uH.

Reliability:

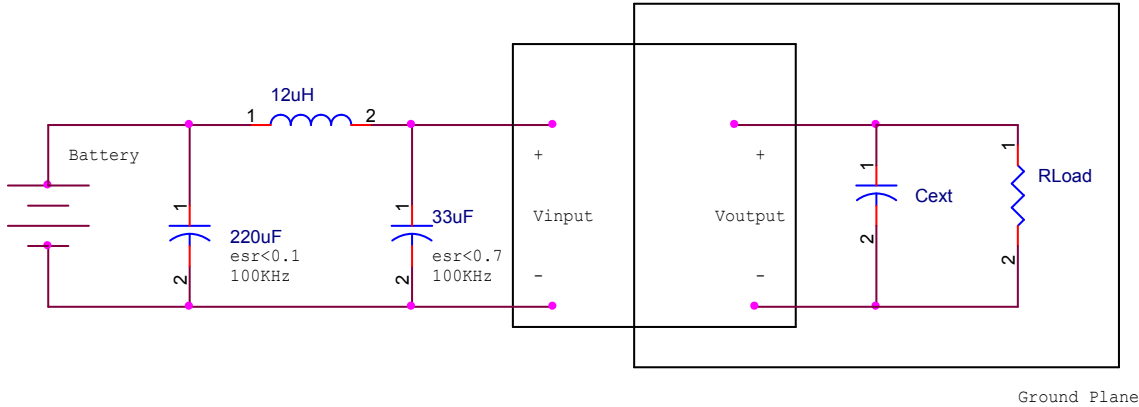
The power modules are designed using TDK Innoveta's stringent design guidelines for component derating, product qualification, and design reviews. Early failures are screened out by both burn-in and an automated final test. The MTBF is calculated to be greater than 4.8M hours at full output power and $T_a = 40^{\circ}\text{C}$ using the Telcordia SR-332 calculation method.

Improper handling or cleaning processes can adversely affect the appearance, testability, and reliability of the power modules. Contact TDK Innoveta technical support for guidance regarding proper handling, cleaning, and soldering of TDK Innoveta's power modules.

Quality:

TDK Innoveta's product development process incorporates advanced quality planning tools such as FMEA and Cpk analysis to ensure designs are robust and reliable. All products are assembled at ISO certified assembly plants.

Input/Output Ripple and Noise Measurements:



The input reflected ripple is measured with a current probe and oscilloscope. The ripple current is the current through the 12uH inductor.

The output ripple measurement is made approximately 9 cm (3.5 in.) from the power module using an oscilloscope and BNC socket. The capacitor Cext is located about 5 cm (2 in.) from the power module; its value varies from code to code and is found on the electrical data page for the power module of interest under the ripple & noise voltage specification in the Notes & Conditions column.

Safety Considerations:

For safety agency approval of the system in which the DC-DC power module is installed, the power module must be installed in compliance with the creepage and clearance requirements of the safety agency. The isolation is basic insulation. For applications requiring basic insulation, care must be taken to maintain minimum creepage and clearance distances when routing traces near the power module.

As part of the production process, the power modules are hi-pot tested from primary and secondary at a test voltage of 1500Vdc.

To preserve maximum flexibility, the power modules are not internally fused. An external input line normal blow fuse with a maximum value of 10A is required by safety agencies. A lower value fuse can be selected based upon the maximum dc input current and maximum inrush energy of the power module.

When the supply to the DC-DC converter is less than 60Vdc, the power module meets all of the requirements for SELV. If the input voltage is a hazardous voltage that exceeds 60Vdc, the output can be considered SELV only if the following conditions are met:

- 1) The input source is isolated from the ac mains by reinforced insulation.
- 2) The input terminal pins are not accessible.
- 3) One pole of the input and one pole of the output are grounded or both are kept floating.
- 4) Single fault testing is performed on the end system to ensure that under a single fault, hazardous voltages do not appear at the module output.

Warranty:

TDK Innoveta’s comprehensive line of power solutions includes efficient, high-density DC-DC converters. TDK Innoveta offers a three-year limited warranty. Complete warranty information is listed on our web site or is available upon request from TDK Innoveta.



Advance Data Sheet: Bellesta® iEA Series –Single Output Eighth Brick



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